

REMARKS

The office action indicates that since claim 1 requires simultaneous read access, not read/write access, the applicants' arguments are not relevant to the cited rejection. However, there is no discussion of any write access in any of the previously submitted remarks. Reconsideration is requested.

To the extent that the ensuing comments under paragraph 2 of the response to arguments suggests something about well known art, the Examiner is respectfully requested to cite a reference. The reason is that nothing in any of the cited art suggests any such thing. Moreover, the reference relied on teaches exactly the opposite thing. Therefore, there is no reason to believe that it is well known that any of the things stated are true.

The suggestion that simultaneous writes to the same address "inherently require logical sequential prioritization to deterministically resolve the logical conflict being introduced in such an event" should be reconsidered. Certainly, the argument is not commensurate with the claims since the claim has nothing to do with writes. Since no reference teaches simultaneous writes and no claim covers simultaneous writes, the argument seems to be both irrelevant and improper.

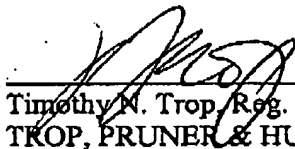
It is not seen how simultaneous writes to the same address would even be possible, much less inherent in the cited reference. Further explanation would be required to establish inherency because, most certainly, nothing in the reference teaches true simultaneous operation of any type, much less simultaneous writes. To be inherent, something must necessarily happen.

Non-simultaneous reads and non-simultaneous writes are both possible and more probable. The suggestion that all simultaneous reads and writes are considered obvious design choices is not defensible. Simultaneous writes to the same address are not claimed, but, certainly if they were claimed, they would not be an obvious design choice.

Therefore, reconsideration is requested.

Respectfully submitted,

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Timothy N. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Ste. 100
Houston, TX 77024
713/468-8880 [Phone]
713/468-8883 [Fax]

Attorneys for Intel Corporation